PCN Number:		20170321	000	PCN Date:	Marc	h 28, 20	017
Title	: Datasheets for	or MSP430FF	۲ *				
Cust	omer Contact:	PCN Manager	<u>anager</u>		Dept:		Quality Services
Chan	ige Type:						
	Assembly Site Design Wafer Bump Site						
	Assembly Process		□ Data Shee	et		Wafer	Bump Material
Assembly Materials			Part numl	per change		Wafer	Bump Process
	☐ Mechanical Specification ☐ Test Site ☐ Wafer Fab Site				Fab Site		
F	☐ Packing/Shipping/Labeling ☐ Test Process ☐ Wafer Fab Materials						
	☐ Wafer Fab Process						
Notification Details							

Description of Change:

Texas Instruments Incorporated is announcing an information only notification.

The product datasheet(s) is being updated as summarized below.

The following change history provides further details.



MSP430FR6979, MSP430FR69791, MSP430FR6977 MSP430FR6928, MSP430FR6927, MSP430FR69271

SLAS797B - AUGUST 2014 - REVISED MARCH 2017

Chan	ges from March 10, 2015 to March 9, 2017 Page
:	Removed "at Rates up to 10 Mbps" from SPI features list item
	Added ACLK as additional multiplexed signal on P7.1 in the PM or RGC package pinout
	Added ACLK as additional multiplexed signal on P7.1
:	Added ACLK as additional multiplexed signal on P7.1
:	Added axis labels to Figure 5-4, LPM3.5 Supply Current vs Temperature (LPM3.5, XT3.7) Added axis labels to Figure 5-5, LPM4.5 Supply Current vs Temperature (LPM4.5) Corrected typos in PACKAGE column of Section 5.12, Thermal Resistance Characteristics
:	Corrected the values of Ψ_{JB} and Ψ_{JT} for all package options in Section 5.12, Thermal Resistance Characteristics 37 Added the note "For more information about traditional and new thermal metrics" in Section 5.12, Thermal Resistance Characteristics 37
:	Added second row to t_{Sample} parameter in Table 5-25, 12-Bit ADC, Timing Parameters
:	Changed the note that starts "Tools that access the Spy-Bi-Wire and BSL interfaces" 67 Updated Section 6.3, Operating Modes, including subsections and tables 69 Changed description in Section 6.4, Interrupt Vector Table and Signatures 72
:	Added Figure 6-1, Interrupt Vectors, Signatures, and Passwords
:	Added Table 6-5, Signatures, and moved contents from Table 6-4
:	Added Table 6-19, LCD Automatic Charge Pump Disable Bits (LCDCPDISx) Added ACLK to P7.1 in PIN NAME column of Table 6-33 Corrected value of "x" column in PJ.7/HFXOUT row Added AESACTL1 register in Table 6-80, AES Accelerator Registers 140
:	Changed the requirements for the capacitor on the RST/NMI pin in Section 7.1.4, Reset



MSP430FR6889, MSP430FR68891, MSP430FR6888, MSP430FR6887 MSP430FR5889, MSP430FR58891, MSP430FR5888, MSP430FR5887

SLASE32B - AUGUST 2014-REVISED MARCH 2017

Ch	nang	les from March 10, 2015 to March 9, 2017	Page
		Removed "at Rates up to 10 Mbps" from SPI features list item	1
		Added Section 3.1, Related Products	
		Added ACLK as additional multiplexed signal on P7.1 in the PZ package pinout	
		Added ACLK as additional multiplexed signal on P7.1 in the PN package pinout	
		Added note that starts "TI recommends connecting"	
		Removed "(divided by 1, 2, 4, or 8)" from ACLK description on P5.2	. 14
		Removed "(divided by 1, 2, 4, or 8)" from ACLK description on PJ.2	
		Added ACLK as additional multiplexed signal on P7.1	
		Removed "(divided by 1, 2, 4, or 8)" from ACLK description on P4.1	
		Removed "(divided by 1, 2, 4, or 8)" from ACLK description on P5.2	
		Removed "(divided by 1, 2, 4, or 8)" from ACLK description on PJ.2	
		Removed "(divided by 1, 2, 4, or 8)" from ACLK description on P4.1	
		Added row for QFN thermal pad	
		Updated the requirements for the capacitor on the RST/NMI pin in Table 4-3, Connection of Unused Pins	28
		Added axis labels to Figure 5-4, LPM3.5 Supply Current vs Temperature (LPM3.5, XT3.7)	36
		Added axis labels to Figure 5-5, LPM4.5 Supply Current vs Temperature (LPM4.5)	
		Corrected typos in PACKAGE column of Section 5.12, Thermal Resistance Characteristics	38
		Corrected the values of Ψ_{JB} and Ψ_{JT} for all package options in Section 5.12, Thermal Resistance Characteristics	38
		Added the note "For more information about traditional and new thermal metrics" in Section 5.12, Thermal	
		Resistance Characteristics	. 38
	•	Added second row to t _{Samole} parameter in Table 5-25, 12-Bit ADC, Timing Parameters	
	•	Removed ADC12DIV from the formula for the t _{CONVERT} TYP time, because ADC12CLK is after division	. 61
	•	Added " R_S < 10 k Ω " to the note that starts "Approximately 10 Tau (τ) are needed" on Table 5-25, 12-Bit ADC,	
		Timing Parameters	
	•	Changed the note that starts "Tools that access the Spy-Bi-Wire and BSL interfaces"	. 72
	•	Updated Section 6.3, Operating Modes, including subsections and tables	
	•	Changed description in Section 6.4, Interrupt Vector Table and Signatures	
	•	Added Figure 6-1, Interrupt Vectors, Signatures, and Passwords	. 77
	•	Changed Table 6-4, Interrupt Sources, Flags, and Vectors: removed Reserved vectors and moved Signatures to	
		Table 6-5	
	•	Added Table 6-5, Signatures, and moved contents from Table 6-4	
	•	Changed all instances of "bootstrap loader" to "bootloader" throughout document	
	•	Corrected spelling of NMIIFG in Table 6-10, System Module Interrupt Vector Registers	
	•	Added Table 6-19, LCD Automatic Charge Pump Disable Bits (LCDCPDISx)	
	•	Added ACLK to P7.1 in PIN NAME column of Table 6-33	
	•	Corrected value of "x" column in PJ.7/HFXOUT row	
	•	Changed the requirements for the capacitor on the RST/NMI pin in Section 7.1.4, Reset	
	•	Replaced former section Development Tools Support with Section 8.3, Tools and Software	
	•	Updated Section 8.4, Documentation Support	165



MSP430FR6879, MSP430FR68791, MSP430FR6877

SLASE33B -AUGUST 2014-REVISED MARCH 2017

Changes from March 10, 2015 to March 9, 2017

Page

•	Removed "at Rates up to 10 Mbps" from SPI features list item	. 1
•	Added Section 3.1, Related Products	. 7
•	Added ACLK as additional multiplexed signal on P7.1 in the PZ package pinout	. 8
•	Added ACLK as additional multiplexed signal on P7.1 in the PN package pinout	. 9
•	Removed "(divided by 1, 2, 4, or 8)" from ACLK description on P5.2	12
•	Removed "(divided by 1, 2, 4, or 8)" from ACLK description on PJ.2	12
•	Added ACLK as additional multiplexed signal on P7.1.	
•	Removed "(divided by 1, 2, 4, or 8)" from ACLK description on P4.1	18
•	Updated the requirements for the capacitor on the RST/NMI pin in Table 4-2, Connection of Unused Pins	19
•	Added axis labels to Figure 5-4, LPM3.5 Supply Current vs Temperature (LPM3.5, XT3.7)	27
•	Added axis labels to Figure 5-5, LPM4.5 Supply Current vs Temperature (LPM4.5)	
•	Corrected typos in PACKAGE column of Section 5.12, Thermal Resistance Characteristics	29
•	Corrected the values of Ψ_{JB} and Ψ_{JT} for all package options in Section 5.12, Thermal Resistance Characteristics	29
•	Added the note "For more information about traditional and new thermal metrics" in Section 5.12, Thermal	
		29
•	Added second row to t _{Sample} parameter in Table 5-25, 12-Bit ADC, Timing Parameters	52
•	Removed ADC12DIV from the formula for the t _{CONVERT} TYP time, because ADC12CLK is after division	52
•	Added "R _S < 10 k Ω " to the note that starts "Approximately 10 Tau (τ) are needed" on Table 5-25, 12-Bit ADC,	
	Timing Parameters	
•	Changed the note that starts "Tools that access the Spy-Bi-Wire and BSL interfaces"	59
•	Updated Section 6.3, Operating Modes, including subsections and tables	61
•	Changed description in Section 6.4, Interrupt Vector Table and Signatures	
•	Added Figure 6-1, Interrupt Vectors, Signatures, and Passwords	64
•	Changed Table 6-4, Interrupt Sources, Flags, and Vectors: removed Reserved vectors and moved Signatures to	
	Table 6-5	65
•	Added Table 6-5, Signatures, and moved contents from Table 6-4	
•	Changed all instances of "bootstrap loader" to "bootloader" throughout document	67
•	Corrected spelling of NMIIFG in Table 6-10, System Module Interrupt Vector Registers	
•	Added Table 6-19, LCD Automatic Charge Pump Disable Bits (LCDCPDISx)	78
•	Added ACLK to P7.1 in PIN NAME column of Table 6-33.	97
•	Corrected value of "x" column in PJ.7/HFXOUT row	112
•	Changed the requirements for the capacitor on the RST/NMI pin in Section 7.1.4, Reset	138
•	Replaced former section Development Tools Support with Section 8.3, Tools and Software	145
•	Updated Section 8.4, Documentation Support	147



MSP430FR6989, MSP430FR69891, MSP430FR6988, MSP430FR6987 TEXAS INSTRUMENTS MSP430FR6989, MSP430FR69891, MSP430FR6989, MSP430FR6989, MSP430FR69891, MSP430FR69891, MSP430FR59891, MSP430FR5988, MSP430FR5986

SLAS789C – JUNE 2014–REVISED MARCH 2017

Page

•	Removed "at Rates up to 10 Mbps" from SPI features list item	1
•	Added Added AES256 to block diagram for MSP430FR598x, MSP430FR598x1.	4
•	Added Section 3.1, Related Products	
	Added ACLK as additional multiplexed signal on P7.1 in the PZ package pinout	
	Added ACLK as additional multiplexed signal on P7.1 in the PN package pinout	
	Added note that starts "TI recommends connecting"	
	Removed "(divided by 1, 2, 4, or 8)" from ACLK description on P5.2	14
	Removed "(divided by 1, 2, 4, or 8)" from ACLK description on PJ.2	
	Added ACLK as additional multiplexed signal on P7.1.	
	Removed "(divided by 1, 2, 4, or 8)" from ACLK description on P4.1	
	Removed "(divided by 1, 2, 4, or 8)" from ACLK description on P5.2.	
	Removed "(divided by 1, 2, 4, or 8)" from ACLK description on PJ.2	
	Removed "(divided by 1, 2, 4, or 8)" from ACLK description on P4.1	
	Added row for QFN thermal pad	
	Updated the requirements for the capacitor on the RST/NMI pin in Table 4-3, Connection of Unused Pins	28
	Added axis labels to Figure 5-4, LPM3.5 Supply Current vs Temperature (LPM3.5, XT3.7)	
	Added axis labels to Figure 5-5, LPM4.5 Supply Current vs Temperature (LPM4.5)	
	Corrected typos in PACKAGE column of Section 5.12, Thermal Resistance Characteristics	
	Corrected the values of Ψ_{JB} and Ψ_{JT} for all package options in Section 5.12, Thermal Resistance Characteristics	
	Added the note "For more information about traditional and new thermal metrics" in Section 5.12, <i>Thermal</i>	50
	Resistance Characteristics	30
	Added second row to t _{Sample} parameter in Table 5-25, 12-Bit ADC, Timing Parameters	
	Removed ADC12DIV from the formula for the t _{CONVERT} TYP time, because ADC12CLK is after division	
	Added "R _S < 10 k Ω " to the note that starts "Approximately 10 Tau (τ) are needed" on Table 5-25, 12-Bit ADC,	01
	Timing Parameters	61
	Changed the note that starts "Tools that access the Spy-Bi-Wire and BSL interfaces"	
	Updated Section 6.3, Operating Modes, including subsections and tables	
	Changed description in Section 6.4, Interrupt Vector Table and Signatures	
	Added Figure 6-1, Interrupt Vectors, Signatures, and Passwords	77
:	Changed Table 6-4, Interrupt Sources, Flags, and Vectors: removed Reserved vectors and moved Signatures to	11
•		70
	Table 6-5	10
	Added Table 6-5, Signatures, and moved contents from Table 6-4.	79
	Changed all instances of "bootstrap loader" to "bootloader" throughout document	
	Corrected spelling of NMIIFG in Table 6-10, System Module Interrupt Vector Registers	
	Added Table 6-19, LCD Automatic Charge Pump Disable Bits (LCDCPDISx)	
	Added ACLK to P7.1 in PIN NAME column of Table 6-33	
•	Corrected value of "x" column in PJ.7/HFXOUT row	
•	Added AESACTL1 register in Table 6-80, AES Accelerator Registers.	
•	Changed the requirements for the capacitor on the RST/NMI pin in Section 7.1.4, Reset	
•	Replaced former section Development Tools Support with Section 8.3, Tools and Software	
	Undated Section 8.4. Documentation Support	165

The datasheet n	number will	be	changing.
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Device Family	Change From:	Change To:
MSP430FR6979, MSP430FR69791,	SLAS797A	SLAS797B
MSP430FR6977		
MSP430FR6928, MSP430FR6927,		
MSP430FR69271		
MSP430FR6889, MSP430FR68891,	SLASE32A	SLASE32B
MSP430FR6888, MSP430FR6887		
MSP430FR5889, MSP430FR58891,		
MSP430FR5888, MSP430FR5887		
MSP430FR6879, MSP430FR68791,	SLASE33A	SLASE33B
MSP430FR6877		
MSP430FR6989, MSP430FR69891,	SLAS789B	SLAS789C
MSP430FR6988, MSP430FR6987		
MSP430FR5989, MSP430FR59891,		
MSP430FR5988, MSP430FR5987,		
MSP430FR5986		

These changes may be reviewed at the datasheet links provided.

http://www.ti.com/product/MSP430FR6979

http://www.ti.com/product/MSP430FR6889

http://www.ti.com/product/MSP430FR6879

http://www.ti.com/product/MSP430FR6989

Reason for Change:

To accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:

None.

Product Affected:

MSP430FR69271IPM	MSP430FR69271IPMR	MSP430FR69271IRGCR	MSP430FR69271IRGCT
MSP430FR6927IPM	MSP430FR6927IPMR	MSP430FR6927IRGCR	MSP430FR6927IRGCR
MSP430FR6928IPM	MSP430FR6928IPMR	MSP430FR6977IPN	MSP430FR6977IPNR
MSP430FR6977IPZ	MSP430FR6977IPZR	MSP430FR69791IPN	MSP430FR69791IPNR
MSP430FR69791IPZ	MSP430FR69791IPZR	MSP430FR6979IPN	MSP430FR6979IPNR
MSP430FR6979IPZ	MSP430FR6979IPZR	MSP430FR5887IPM	MSP430FR5887IPMR
MSP430FR5887IRGCR	MSP430FR5887IRGCT	MSP430FR5888IPM	MSP430FR5888IPMR
MSP430FR5888IRGCR	MSP430FR5888IRGCT	MSP430FR58891IPM	MSP430FR58891IPMR
MSP430FR58891IRGCR	MSP430FR58891IRGCT	MSP430FR5889IPM	MSP430FR5889IPMR
MSP430FR5889IRGCR	MSP430FR5889IRGCT	MSP430FR6887IPN	MSP430FR6887IPNR
MSP430FR6887IPZ	MSP430FR6887IPZR	MSP430FR6888IPN	MSP430FR6888IPNR
MSP430FR6888IPZ	MSP430FR6888IPZR	MSP430FR68891IPN	MSP430FR68891IPNR
MSP430FR68891IPZ	MSP430FR68891IPZR	MSP430FR6889IPN	MSP430FR6889IPNR
MSP430FR6889IPZ	MSP430FR6889IPZR	MSP430FR6877IPN	MSP430FR6877IPNR
MSP430FR6877IPZ	MSP430FR6877IPZR	MSP430FR68791IPN	MSP430FR68791IPNR

MSP430FR68791IPZ	MSP430FR68791IPZR	MSP430FR6879IPN	MSP430FR6879IPNR
MSP430FR6879IPZ	MSP430FR6879IPZR	MSP430FR5986IPM	MSP430FR5986IPMR
MSP430FR5987IPM	MSP430FR5987IPMR	MSP430FR5987IRGCR	MSP430FR5987IRGCT
MSP430FR5988IPM	MSP430FR5988IPMR	MSP430FR5988IRGCR	MSP430FR5988IRGCT
MSP430FR59891IPM	MSP430FR59891IPMR	MSP430FR59891IRGCR	MSP430FR59891IRGCT
MSP430FR5989IPM	MSP430FR5989IPMR	MSP430FR5989IRGCR	MSP430FR5989IRGCT
MSP430FR6987IPN	MSP430FR6987IPNR	MSP430FR6987IPZ	MSP430FR6987IPZR
MSP430FR6988IPN	MSP430FR6988IPNR	MSP430FR6988IPZ	MSP430FR6988IPZR
MSP430FR69891IPN	MSP430FR69891IPNR	MSP430FR69891IPZ	MSP430FR69891IPZR
MSP430FR6989IPN	MSP430FR6989IPNR	MSP430FR6989IPZ	MSP430FR6989IPZR

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

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